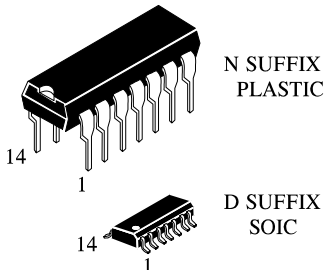


**IN74HC02A**

**Quad 2-Input NOR Gate**

The IN74HC02A is identical in pinout to the LS/ALS02. The device inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with LS/ALSTTL outputs.

- Outputs Directly Interface to CMOS, NMOS, and TTL
- Operating Voltage Range: 2.0 to 6.0 V
- Low Input Current: 1.0  $\mu$ A
- High Noise Immunity Characteristic of CMOS Devices



N SUFFIX  
PLASTIC

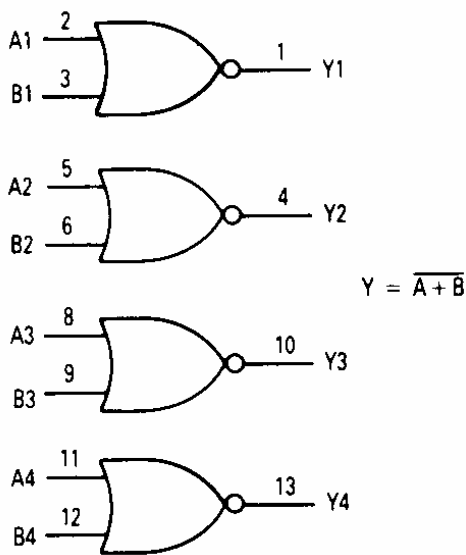
D SUFFIX  
SOIC

**ORDERING INFORMATION**

IN74HC02AN Plastic  
IN74HC02AD SOIC

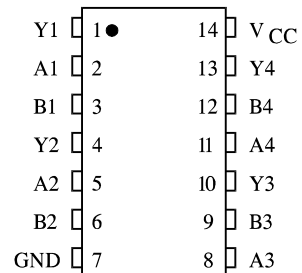
$T_A = -55^\circ$  to  $125^\circ$  C for all packages

**LOGIC DIAGRAM**



PIN 14 =  $V_{CC}$   
PIN 7 = GND

**PIN ASSIGNMENT**



**FUNCTION TABLE**

| Inputs |   | Output |
|--------|---|--------|
| A      | B | Y      |
| L      | L | H      |
| L      | H | L      |
| H      | L | L      |
| H      | H | L      |

**MAXIMUM RATINGS\***

| Symbol           | Parameter  | Value                        | Unit |
|------------------|--|------------------------------|------|
| V <sub>CC</sub>  | DC Supply Voltage (Referenced to GND)  | -0.5 to +7.0                 | V    |
| V <sub>IN</sub>  | DC Input Voltage (Referenced to GND)   | -1.5 to V <sub>CC</sub> +1.5 | V    |
| V <sub>OUT</sub> | DC Output Voltage (Referenced to GND)  | -0.5 to V <sub>CC</sub> +0.5 | V    |
| I <sub>IN</sub>  | DC Input Current, per Pin  | ±20                          | mA   |
| I <sub>OUT</sub> | DC Output Current, per Pin   | ±25                          | mA   |
| I <sub>CC</sub>  | DC Supply Current, V <sub>CC</sub> and GND Pins                                  | ±50                          | mA   |
| P <sub>D</sub>   | Power Dissipation in Still Air, Plastic DIP*<br>SOIC Package*                    | 750<br>500                   | mW   |
| T <sub>stg</sub> | Storage Temperature  | -65 to +150                  | °C   |
| T <sub>L</sub>   | Lead Temperature, 1 mm from Case for 10 Seconds<br>(Plastic DIP or SOIC Package) | 260                          | °C   |

\*Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

\*\*Derating - Plastic DIP: - 10 mW/°C from 65° to 125°C  
SOIC Package: : - 7 mW/°C from 65° to 125°C

**RECOMMENDED OPERATING CONDITIONS**

| Symbol                             | Parameter  | Min | Max             | Unit |
|------------------------------------|--|-----|-----------------|------|
| V <sub>CC</sub>                    | DC Supply Voltage (Referenced to GND)                | 2.0 | 6.0             | V    |
| V <sub>IN</sub> , V <sub>OUT</sub> | DC Input Voltage, Output Voltage (Referenced to GND) | 0   | V <sub>CC</sub> | V    |
| T <sub>A</sub>                     | Operating Temperature, All Package Types             | -55 | +125            | °C   |
| t <sub>r</sub> , t <sub>f</sub>    | Input Rise and Fall Time (Figure 1)                  |     |                 |      |
|                                    | V <sub>CC</sub> =2.0 V                               | 0   | 1000            | ns   |
|                                    | V <sub>CC</sub> =4.5 V                               | 0   | 500             |      |
|                                    | V <sub>CC</sub> =6.0 V                               | 0   | 400             |      |

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation, V<sub>IN</sub> and V<sub>OUT</sub> should be constrained to the range GND ≤ (V<sub>IN</sub> or V<sub>OUT</sub>) ≤ V<sub>CC</sub>.

Unused inputs must always be tied to an appropriate logic voltage level (e.g., either GND or V<sub>CC</sub>). Unused outputs must be left open.

**DC ELECTRICAL CHARACTERISTICS**(Voltages Referenced to GND)

| Symbol          | Parameter  | Test Conditions  | V <sub>CC</sub><br>V | Guaranteed Limit     |           |            | Unit |
|-----------------|--|--|----------------------|----------------------|-----------|------------|------|
|                 |  |  |                      | 25 °C<br>to<br>-55°C | ≤85<br>°C | ≤125<br>°C |      |
| V <sub>IL</sub> | Maximum Low -Level<br>Input Voltage                  | V <sub>OUT</sub> =0.1 V or<br>V <sub>CC</sub> =0.1 V<br>I <sub>OUT</sub> ≤ 20 μA   | 2.0                  | 0.5                  | 0.5       | 0.5        | V    |
|                 |  |  | 4.5                  | 1.35                 | 1.35      | 1.35       |      |
|                 |  |  | 6.0                  | 1.8                  | 1.8       | 1.8        |      |
| V <sub>IH</sub> | Minimum High-Level<br>Input Voltage                  | V <sub>OUT</sub> =0.1 V or<br>V <sub>CC</sub> =0.1 V<br>I <sub>OUT</sub> ≤ 20 μA   | 2.0                  | 1.5                  | 1.5       | 1.5        | V    |
|                 |  |  | 4.5                  | 3.15                 | 3.15      | 3.15       |      |
|                 |  |  | 6.0                  | 4.2                  | 4.2       | 4.2        |      |
| V <sub>OL</sub> | Maximum Low-Level<br>Output Voltage                  | V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OUT</sub> ≤ 20 μA    | 2.0                  | 0.1                  | 0.1       | 0.1        | V    |
|                 |  |  | 4.5                  | 0.1                  | 0.1       | 0.1        |      |
|                 |  |  | 6.0                  | 0.1                  | 0.1       | 0.1        |      |
| V <sub>OH</sub> | Minimum High-Level<br>Output Voltage                 | V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OUT</sub> ≤ 4.0 mA   | 4.5                  | 0.26                 | 0.33      | 0.4        | V    |
|                 |  |  | 6.0                  | 0.26                 | 0.33      | 0.4        |      |
|                 |  |  | 6.0                  | 0.26                 | 0.33      | 0.4        |      |
| V <sub>OH</sub> | Minimum High-Level<br>Output Voltage                 | V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OUT</sub> ≤ 5.2 mA   | 2.0                  | 1.9                  | 1.9       | 1.9        | V    |
|                 |  |  | 4.5                  | 4.4                  | 4.4       | 4.4        |      |
|                 |  |  | 6.0                  | 5.9                  | 5.9       | 5.9        |      |
| V <sub>OH</sub> | Minimum High-Level<br>Output Voltage                 | V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OUT</sub> ≤ 4.0 mA   | 4.5                  | 3.98                 | 3.84      | 3.7        | V    |
|                 |  |  | 6.0                  | 5.48                 | 5.34      | 5.2        |      |
|                 |  |  | 6.0                  | 5.48                 | 5.34      | 5.2        |      |
| I <sub>IL</sub> | Maximum Low-Level<br>Input Leakage Current           | V <sub>IL</sub> =GND   | 6.0                  | -0.1                 | -1.0      | -1.0       | μA   |
| I <sub>IH</sub> | Maximum High-Level<br>Input Leakage Current          | V <sub>IH</sub> =V <sub>CC</sub>   | 6.0                  | 0.1                  | 1.0       | 1.0        | μA   |
| I <sub>CC</sub> | Maximum Quiescent<br>Supply Current<br>(per Package) | V <sub>IL</sub> =V <sub>CC</sub><br>V <sub>IH</sub> =GND<br>I <sub>OUT</sub> =0 μA | 6.0                  | 1.0                  | 10        | 40         | μA   |

**AC ELECTRICAL CHARACTERISTICS**

| Symbol                              | Parameter  | Test Conditions  | V <sub>CC</sub><br>V | Guaranteed Limit                              |                 |                 | Unit |
|-------------------------------------|--|--|----------------------|---|-----------------|-----------------|------|
|                                     |  |  |                      | 25 °C<br>to<br>-55°C                          | ≤85°C           | ≤125°C          |      |
| t <sub>PLH</sub> , t <sub>PHL</sub> | Maximum Propagation Delay,<br>Input A or B to Output Y (Figures 1 and 2)   | V <sub>IL</sub> =0 V<br>V <sub>IH</sub> =V <sub>CC</sub><br>t <sub>LH</sub> =t <sub>HL</sub> =6 ns<br>C <sub>L</sub> = 50 pF | 2.0<br>4.5<br>6.0    | 80<br>16<br>14                                | 100<br>20<br>17 | 120<br>24<br>20 | ns   |
| t <sub>TLH</sub> , t <sub>THL</sub> | Maximum Output Transition<br>Time, Any Output (Figures 1 and 2)  | V <sub>IL</sub> =0 V<br>V <sub>IH</sub> =V <sub>CC</sub><br>t <sub>LH</sub> =t <sub>HL</sub> =6 ns<br>C <sub>L</sub> = 50 pF | 2.0<br>4.5<br>6.0    | 75<br>15<br>13                                | 95<br>19<br>16  | 110<br>22<br>19 | ns   |
| C <sub>IN</sub>                     | Maximum Input Capacitance  |  | 6.0                  | 10  | 10              | 10              | pF   |
| C <sub>PD</sub>                     | Power Dissipation Capacitance<br>(Per Gate) Used to determine the<br>no-load dynamic power<br>consumption:<br>P <sub>D</sub> =C <sub>PD</sub> V <sub>CC</sub> <sup>2</sup> f+I <sub>CC</sub> V <sub>CC</sub> |  | 5.0                  | T <sub>A</sub> = 25°C, V <sub>CC</sub> =5.0 V |                 |                 | pF   |
|                                     |  |  |                      | 22  |                 |                 |      |

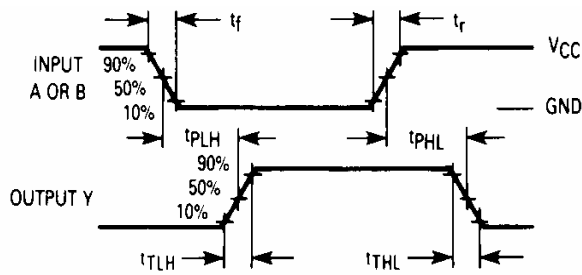
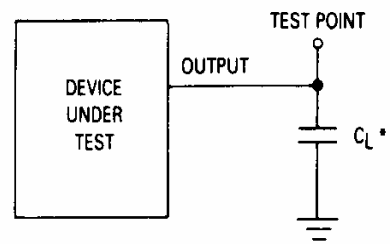


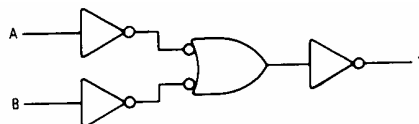
Figure 1. Switching Waveforms



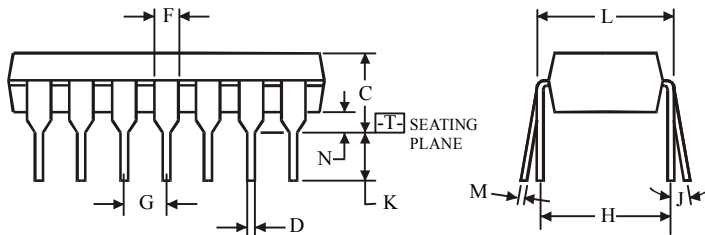
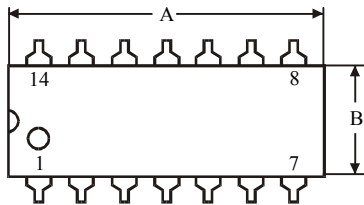
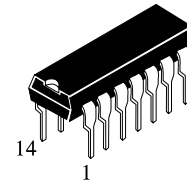
\*Includes all probe and jig capacitance.

Figure 2. Test Circuit

**EXPANDED LOGIC DIAGRAM  
(1/4 of the Device)**



**N SUFFIX PLASTIC DIP  
(MS - 001AA)**



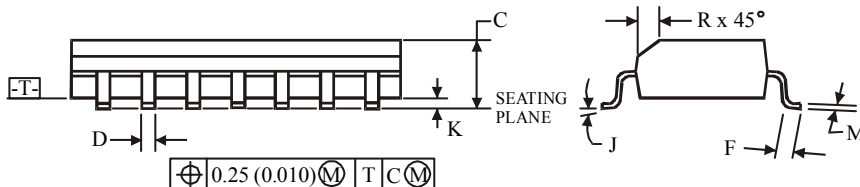
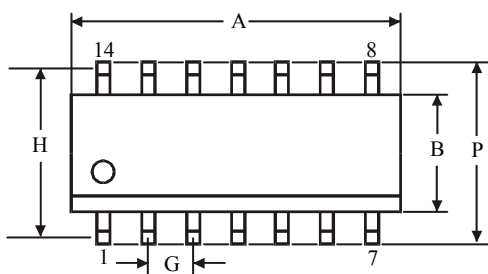
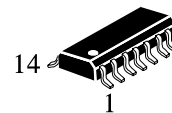
$\oplus 0.25 (0.010) \text{ (M) T}$

**NOTES:**

- Dimensions "A", "B" do not include mold flash or protrusions.  
Maximum mold flash or protrusions 0.25 mm (0.010) per side.

| Symbol | Dimension, mm |       |
|--------|---------------|-------|
|        | MIN           | MAX   |
| A      | 18.67         | 19.69 |
| B      | 6.1           | 7.11  |
| C      |               | 5.33  |
| D      | 0.36          | 0.56  |
| F      | 1.14          | 1.78  |
| G      | 2.54          |       |
| H      | 7.62          |       |
| J      | 0°            | 10°   |
| K      | 2.92          | 3.81  |
| L      | 7.62          | 8.26  |
| M      | 0.2           | 0.36  |
| N      | 0.38          |       |

**D SUFFIX SOIC  
(MS - 012AB)**



$\oplus 0.25 (0.010) \text{ (M) T C (M)}$

**NOTES:**

- Dimensions A and B do not include mold flash or protrusion.
- Maximum mold flash or protrusion 0.15 mm (0.006) per side for A; for B - 0.25 mm (0.010) per side.

| Symbol | Dimension, mm |      |
|--------|---------------|------|
|        | MIN           | MAX  |
| A      | 8.55          | 8.75 |
| B      | 3.8           | 4    |
| C      | 1.35          | 1.75 |
| D      | 0.33          | 0.51 |
| F      | 0.4           | 1.27 |
| G      | 1.27          |      |
| H      | 5.27          |      |
| J      | 0°            | 8°   |
| K      | 0.1           | 0.25 |
| M      | 0.19          | 0.25 |
| P      | 5.8           | 6.2  |
| R      | 0.25          | 0.5  |